

## Intel based Micro-Server for Data centers





The objective was to design and develop Intel Xeon and Atom based processor modules for micro server



Requirement involved developing BIOS for each modules and a production test jig.



Bring up and Design Validation Tests (DVT) on test jig





Atom and Xeon Server SoC based processor modules built for a 2U chassis with 4 SODIMM slots



PCIe Gen 3 based active backplane and switch fabric interface



BIOS development for Xeon and Atom, Bring-up and Design Validation Tests (DVT)





OrCAD capture, Cadence Allegro, Concept HDL



Intel Xeon (Broadwell-DE), Intel Atom (Avoton), SSD over M.2, DDR4 SODIMMs, PCIe Gen3 fabric interface, headless design



Stack up: 14 and 16 layer, low cost PCB avoiding the use of HDIs.





Enabled the customer to create a product for low power, elastic demands and high performance applications.



Complies to Open
Networking Platform specs
to enable the hardware as
base infrastructure for
running Virtual Network
Functions and hence create
market in NFV space.



Unified architecture also allows the product to be positioned for web scale data centers.



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